

S210-R range

Revision: 7R17.10.02
Sheet: B-210-00

FEATURES

Input voltage

115/230Vac $\pm 15\%$
(jumper selectable on pcb)

Input frequency

50/60Hz

Efficiency

75% typ.

Switching operating frequency

50KHz ca.

Input protections

- Inrush current limitation : 30A at Vin=220Vac
- EMI filter
- Line fuse

Leakage current to GND

Max 2mA at 50Hz

See table for

- Output voltages and currents
- Line and load regulation
- Output ripple and noise

Output protections

- Overload protection
- Short circuit protection
- Overvoltage: at Vo + 25% typ.

Hold up time

15msec min.

Start up time

60msec typ.

Output power

200÷240W (see table)

Remote sense compensation

- 0.5V max

Output signals

- Alarm relay contact (U.V.P)

Inhibit input

- TTL/CMOS comp. low active

Operating temperature

0°C to 50°C

Temperature power derating

2%/°C (50÷70°C)

Storage temperature

-20°C to 85°C

Temperature drift

0.01%/°C

Cooling

Natural convection

Control and adjustment

- Vadjust trimmer on front panel

Test points

Output voltage test-points
Balance signal test-point (opt.)

Operating indicators

- Led Vout OK on front panel

Dielectric withstand voltage

- Input - Output : 3750Vac (on insul.comp.)
- Input - P.E.: 1750Vac

Isolation

- Output - P.E.: 500Vdc

Comply with

- EN 50081-1, EN 61000-6-2, EN 60950
- CE

Weight

1660g

Optional features

- BAL - Load balance adjust. for parallel connection
- DC - DC input for AC-DC units
- DD - Output decoupl. diode for parallel connection
- PF - Power fail/reset signals
- PROG - Programmable Vout

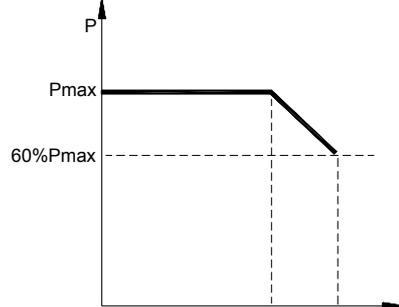
FEATURES TABLE

MODEL	Output Power W	Vout Volts	Output voltage adj. Volts	Iout Ampere	Line regulation VIN(min÷max) %	Load regulation (10÷100%) %	Ripple & Noise (0÷30MHz) % Vout
S212R	200	5	4.5÷5.5	40	± 0.1	± 0.5	1
S213R	216	12	10.5÷14	18	± 0.1	± 0.5	1
S214R	240	15	14÷17	16	± 0.1	± 0.5	1
S216R	240	24	20÷28	10	± 0.1	± 0.5	1
S217R	240	36	31÷42	6	± 0.1	± 0.5	1
S218R	240	48	41÷52	5	± 0.1	± 0.5	1
S219R	240	96	85÷110	2.5	± 0.1	± 0.5	1

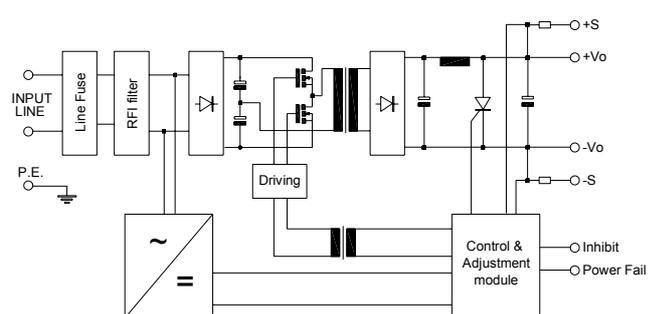
S210-R VIEW



TEMP. POWER DERATING

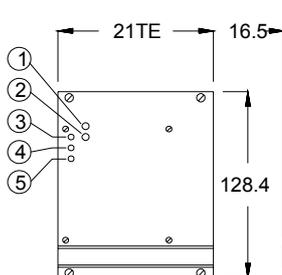


BLOCK DIAGRAM

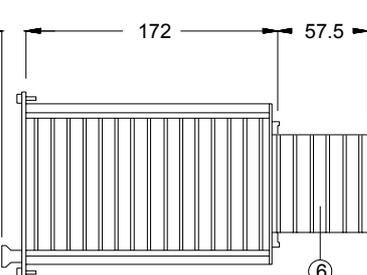


DIMENSIONS AND CONNECTIONS

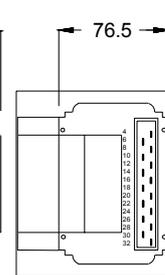
front view



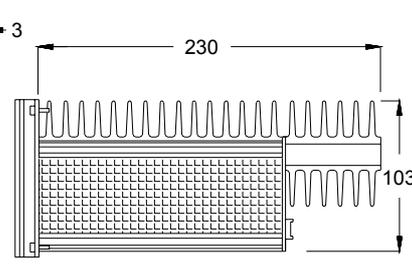
side view



rear view



bottom view



- 04) Sense+
- 06) Vout+
- 08) Vout+
- 10) Vout+
- 12) Vout-
- 14) Vout-
- 16) Vout-
- 18) Sense-
- 20) Relay N.O.
- 22) Relay Com
- 24) Inhibit
- 26) Power fail
- 28) ACinputL
- 30) ACinputN
- 32) P.E./Chassis

- 1) Vout adjustment
- 2) Led Vout-ok
- 3) Test point+
- 4) Test point-
- 5) Balance signal(opt.)
- 6) External heatsink only if Output series diode option (DD) is required (only 5V out)

Note: all features are subject to change without notice.

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